

**Notice of References Cited**

Application/Control No.

10/010,343

Applicant(s)/Patent Under  
Reexamination  
YEGNASHANKARAN ET AL.

Examiner

Leonardo Andújar

Art Unit

2826

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,399,416 ✓	06-2002	Wark, James M.	438/106
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Wolf et al., Silicon Processing for the VLSI Era, 2000, Lattice Press, Vol. 2, 826-829. ✓			
	V				
	W				
	X				

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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